



# **SDBN500B0**1

### 500mA NPN TRANSISTOR SWITCH WITH SNUBBER DIODE

### **General Description**

SDBN500B01 is best suited for switching inductive loads in power switching applications. It improves efficiency and reliability of power switching systems and it can support continuous maximum current of 500 mA. It features NPN transistor with high breakdown voltage and discrete switching diode with high forward surge current. It reduces component count, consumes less space and minimizes parasitic losses. The component devices can be used as a part of a circuit or as a stand

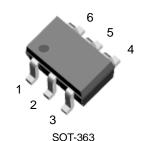
# alone discrete device.

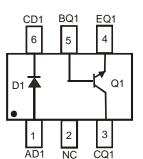
### **Features**

- NPN Transistor with High Break-Down Voltage
- Switching Diode with High Forward Surge
- Low Switching and Conduction Losses
- Surface Mount Package Suited for Automated Assembly
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

### **Mechanical Data**

- Case: SOT-363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Figure
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Marking and Type Code Information: See Page 6
- Ordering Information: See Page 6
- Weight: 0.016 grams (approximate)





Schematic and Pin Configuration

Sub-Component P/N	Reference	Device Type
MMBTA06_DIE	Q1	NPN Transistor
BAS31_DIE	D1	Switching Diode

### Maximum Ratings: Total Device @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3)	$P_d$	200	mW
Power Derating Factor above 25°C	P <sub>der</sub>	1.6	mW / °C
Output Current	l <sub>out</sub>	500	mA

### Thermal Characteristics

Characteristic	Symbol	Value	Unit
Junction Operating and Storage Temperature Range	$T_j$ , $T_{STG}$	-55 to +150	°C
Thermal Resistance, Junction to Ambient Air (Note 3) (Equivalent to One Heated Junction of NPN Transistor)	$R_{ hetaJA}$	625	°C/W

- Notes: 1. No purposefully added lead.
  - 2. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
  - 3. Device mounted on FR-4 PCB, 1" x 0.85" x 0.062"; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.



### **Maximum Ratings:**

# Sub-Component Device – Switching Diode (D1) @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage	$V_{RM}$	100	V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	75	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	53	V
Forward Continuous Current (Page 1: Note 3)	I <sub>FM</sub>	500	mA
Average Rectified Output Current (Page 1: Note 3)	I <sub>O</sub>	250	mA
Non-Repetitive Peak Forward Surge Current @ t = 1.0 us	I	4	A
@ t = 1.0 s	IFSM	2	Ā

# Sub Component Device - Discrete NPN Transistor (Q1) @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	80	V
Collector-Emitter Voltage	V <sub>CEO</sub>	80	V
Emitter-Base Voltage	V <sub>EBO</sub>	4	V
Output Current - continuous (Page 1: Note 3)	Ic	500	mA

## **Electrical Characteristics:**

# Switching Diode (D1) @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 4)	V <sub>(BR)R</sub>	75	_	_	V	$I_R = 10 \mu A$
		0.62	_	0.37		$I_F = 5 \text{ mA}$
Forward Voltage Drop (Note 4)	\/	_	_	0.855	V	$I_F = 10 \text{ mA}$
Forward Voltage Drop (Note 4)	V <sub>FM</sub>	_	_	1	,	I <sub>F</sub> = 100 mA
		_	_	1.25		I <sub>F</sub> = 150 mA
		_	_	2.5	Δ	$V_R = 75V$
Reverse Current (Note 4)	1-	_	_	50		V <sub>R</sub> = 75V, T <sub>j</sub> = 150 °C
Reverse Current (Note 4)	I <sub>R</sub>	_	_	30		V <sub>R</sub> = 25V, T <sub>i</sub> = 150 °C
		_	_	25		V <sub>R</sub> = 20V
Total Capacitance	C <sub>T</sub>	_	_	4	pF	$V_R = 0V, f = 1.0 \text{ MHz}$
Reverse Recovery Time	t <sub>rr</sub>	_	_	4	ns	$I_F = I_R = 10 \text{mA}, I_{rr} = 0.1 \text{x} I_R, R_L = 100 \Omega$

Notes: 4. Short duration pulse test used to minimize self-heating effect.



## Discrete NPN Transistor (Q1) @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 4)							
Collector-Base Breakdown Voltage	V <sub>BR(CBO)</sub>	80		V	$I_C = 10 \mu A, I_E = 0$		
Collector-Emitter Breakdown Voltage	V <sub>BR(CEO)</sub>	80	_	V	$I_C = 1.0 \text{ mA}, I_B = 0$		
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	4	_	V	$I_E = 100 \mu A, I_C = 0$		
Collector Cutoff Current	I <sub>CEX</sub>	_	100	nA	$V_{CE} = 60V, V_{EB(OFF)} = 3.0V$		
Base Cutoff Current (I <sub>BEX</sub> )	$I_{BL}$	_	100	nA	$V_{CE} = 60V, V_{EB(OFF)} = 3.0V$		
Collector-Base Cut Off Current	I <sub>CBO</sub>	_	100	nA	$V_{CB} = 80V, I_{E} = 0$		
Collector-Emitter Cut Off Current, IO(OFF)	I <sub>CEO</sub>	_	100	nA	$V_{CE} = 80V, I_B = 0$		
Emitter-Base Cut Off Current	I <sub>EBO</sub>	_	100	nA	$V_{EB} = 5V, I_C = 0$		
ON CHARACTERISTICS (Note 4)							
		60		_	$V_{CE} = 1V$ , $I_{C} = 100 \mu A$		
		80		_	$V_{CE} = 1V$ , $I_C = 1$ mA		
DC Current Gain	h	100	_	_	$V_{CE} = 1V$ , $I_C = 10$ mA		
DC Current Gain	h <sub>FE</sub>	100	_	_	$V_{CE} = 1V, I_{C} = 50 \text{ mA}$		
		90	_	_	$V_{CE} = 1V, I_{C} = 100 \text{ mA}$		
		80	_	_	$V_{CE} = 1V, I_{C} = 200 \text{ mA}$		
			0.1	V	$I_C = 10 \text{ mA}, I_B = 1 \text{ mA}$		
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>		0.25	V	$I_C = 100 \text{ mA}, I_B = 10 \text{ mA}$		
		_	0.35	V	$I_C = 200 \text{ mA}, I_B = 20 \text{ mA}$		
Base-Emitter Turn-on Voltage	V <sub>BE(ON)</sub>	_	0.98	V	$V_{CE} = 5V$ , $I_C = 2 \text{ mA}$		
Base-Emitter Saturation Voltage		_	0.95	V	$I_C = 10 \text{ mA}, I_B = 1 \text{ mA}$		
base-Emilier Saturation voltage	V <sub>BE</sub> (SAT)	_	1.2	V	$I_C = 100 \text{ mA}, V_{CE} = 1V$		
SMALL SIGNAL CHARACTERISTICS							
Output Capacitance	Сово	_	4	pF	$V_{CB} = 5.0 \text{ V}, f = 1.0 \text{MHz}, I_E = 0$		
Input Capacitance	C <sub>IBO</sub>		6	pF	$V_{EB} = 5.0 \text{ V}, f = 1.0 \text{MHz}, I_{C} = 0$		
Current Gain-Bandwidth Product	f⊤	100	_	MHz	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 10mA, f = 100MHz		
SWITCHING CHARACTERISTICS							
Delay Time	t <sub>d</sub>		35	ns	$V_{CC} = 3.0 \text{ V}, I_{C} = 10\text{mA},$		
Rise Time	t <sub>r</sub>		35	ns	$V_{BE(OFF)} = 0.5V, I_{B1} = 1.0mA$		

Pulse Test: Pulse width, tp<300uS, Duty Cycle, d<=2%

Notes: 4. Short duration pulse test used to minimize self-heating effect.

# **Typical Characteristics**

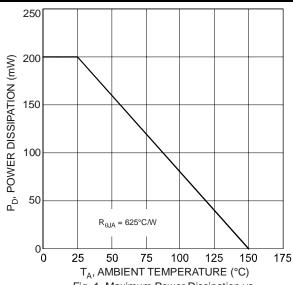
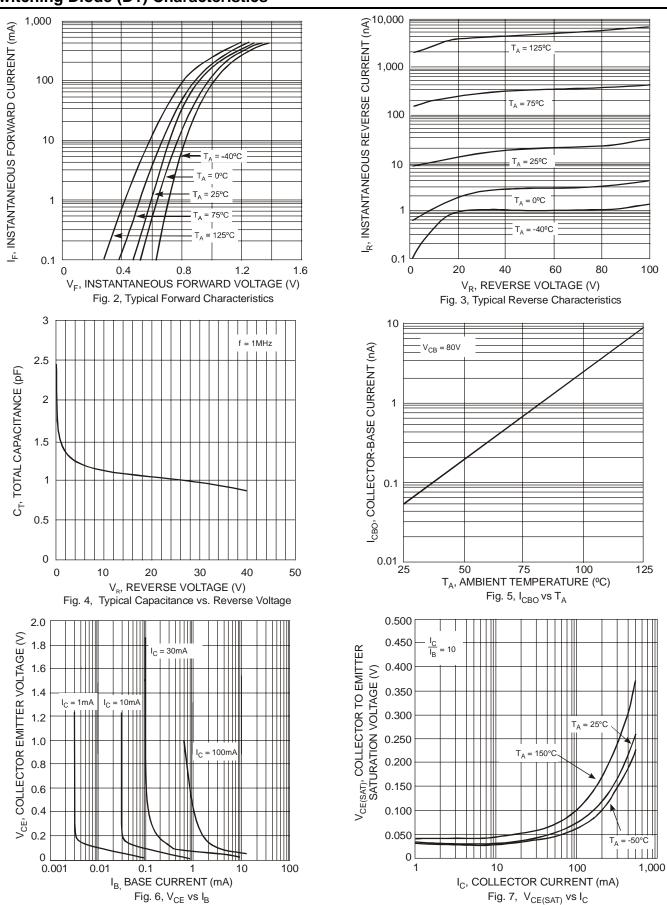


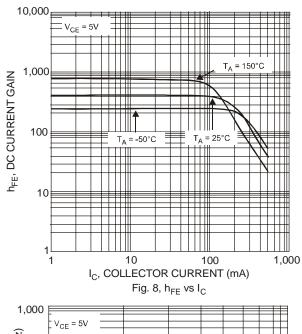
Fig. 1, Maximum Power Dissipation vs. Ambient Temperature

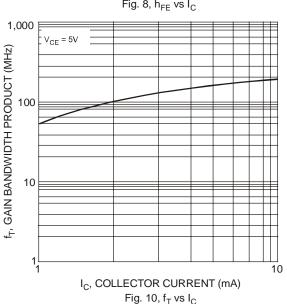


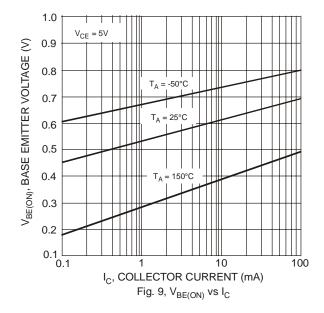
# **Switching Diode (D1) Characteristics**





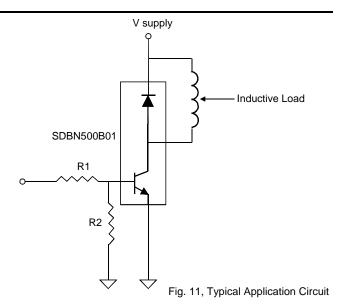






# **Application Details:**

NPN transistor (MMBTA06) and Switching diode (BAV70) integrated as one in SDBN500B01 can be used as a discrete entity for general applications or part of a circuit to function as low side switch for sinking current. NPN is selected based on high break-down voltage and maximum collector current range. Switching diode is selected based on instantaneous forward surge current. The Switching diode dissipates very little power because it is on for only a small portion of the switching cycle. It is designed to replace the discrete NPN transistor and a Switching diode in two separate packages into one small package as shown in Figure. It consumes less board space and also helps to minimize conduction or switching losses due to parasitic inductances (e.g. PCB traces) in power switch applications. (Please see Fig. 11 for one example of typical application circuit used in conjunction with DC-DC converter as a part of the power management system).



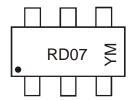


## Ordering Information (Note 5)

Device	evice Marking Code Packaging		Shipping	
SDBN500B01-7	RD07	SOT-363	3000/Tape & Reel	

5. For packaging details, please see below or go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

### **Marking Information**



RD07 = Product Type Marking Code,

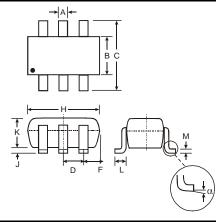
YM = Date Code Marking Y = Year e.g. T = 2006

M = Month e.g. 9 = September

Date Code Key

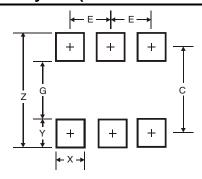
Year	200	6	2007		2008	20	09	2010		2011	2	2012
Code	Т		U		V	V	V	Χ		Υ		Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

### **Mechanical Details**



SOT-363					
Dim	Min	Max			
Α	0.10	0.30			
В	1.15	1.35			
С	2.00	2.20			
D	0.65 N	ominal			
F	0.30	0.40			
Н	1.80	2.20			
J	-	0.10			
K	0.90	1.00			
L	0.25	0.40			
M	0.10	0.25			
α	0°	8°			
All Dim	ensions	in mm			

# Suggested Pad Layout: (Based on IPC-SM-782)



Dimensions	Value			
Z	2.5			
G	1.3			
X	0.42			
Υ	0.6			
С	1.9			
E	0.65			
All Dimensions in mm				

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